

# Approximating **Outside the Processor**

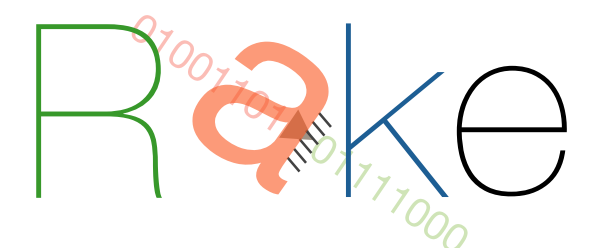
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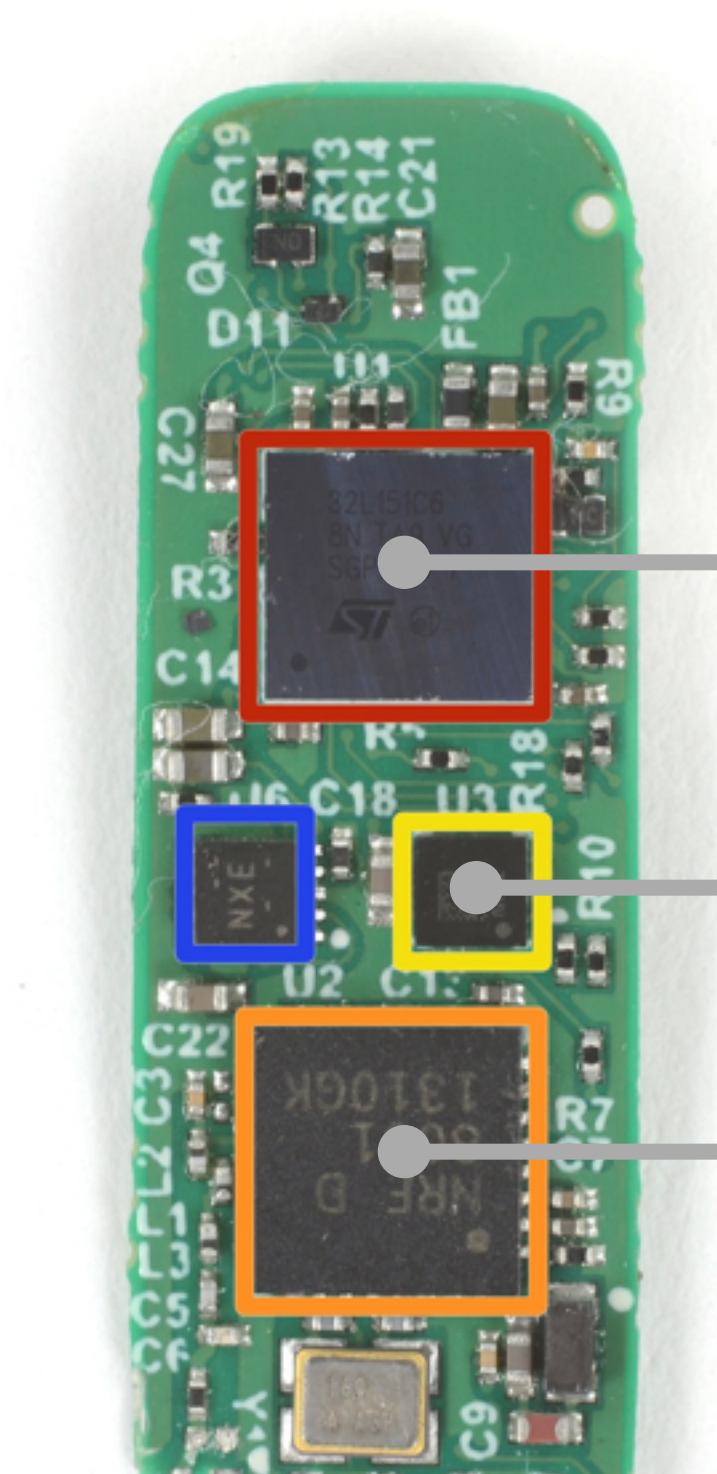
WAX'15, co-located with PLDI'15, 13th June 2015



# Approximating **Outside the Processor**



(Source: Fitbit)



(Source: ifitxit.com)

ARM Cortex M3 Microcontroller

Accelerometer IC

Bluetooth Low-Energy IC

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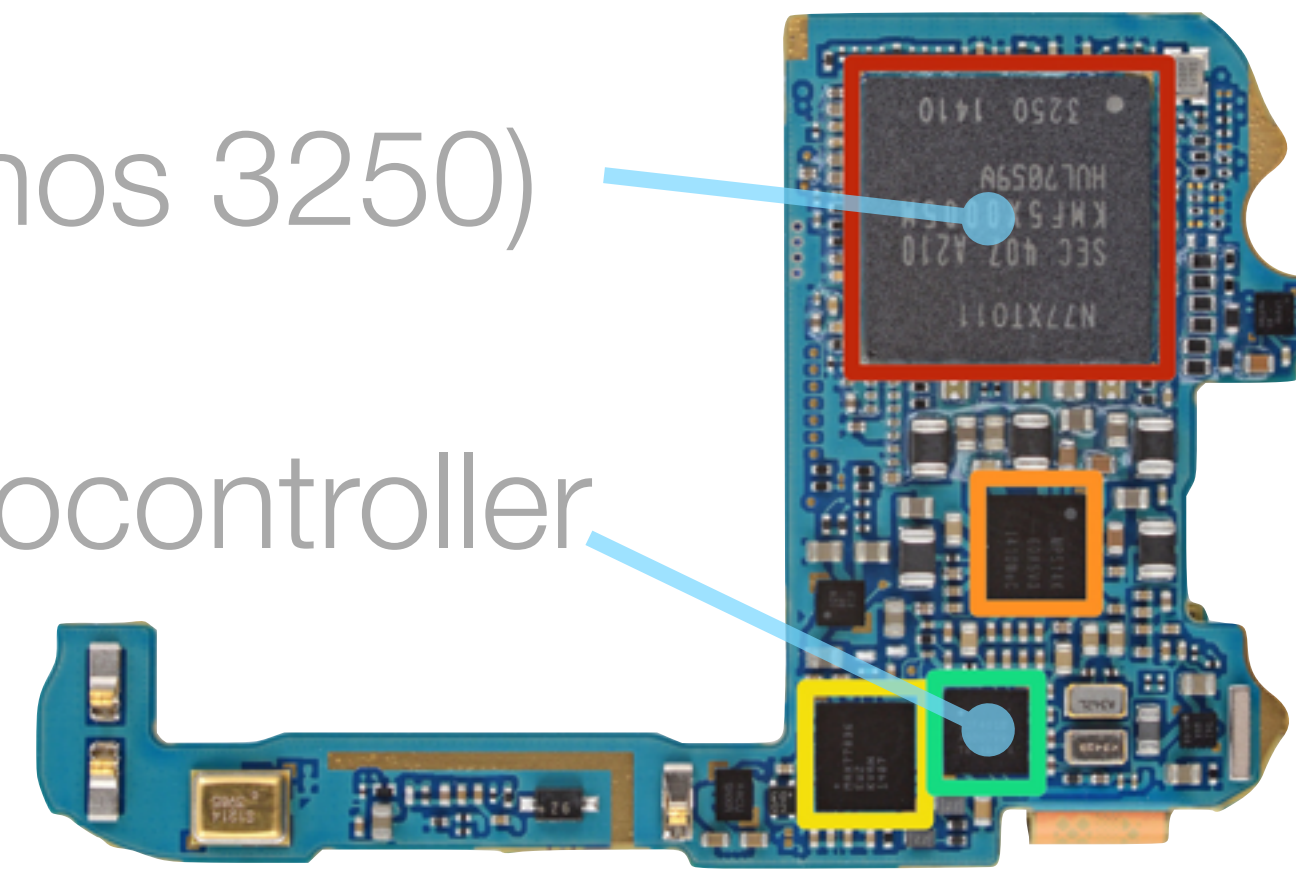


(Source: Samsung)

**320x320 AMOLED Display**, 24 bits per pixel color

Dual-Core ARM (Exynos 3250)

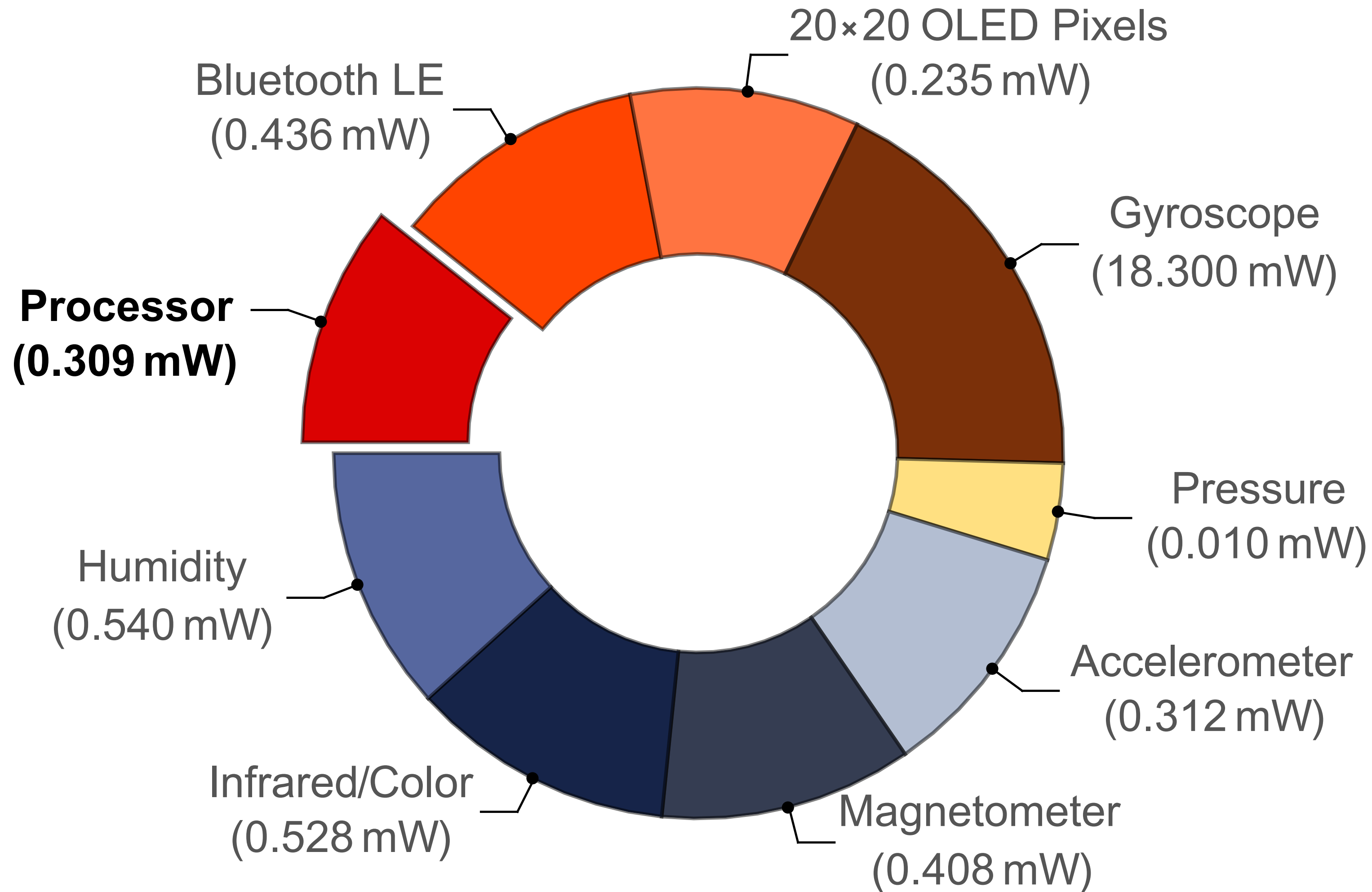
ARM Cortex M4 Microcontroller



(Source: ifitx.com)



# Sensors/Displays Dominate Power Dissipation in Wearables



(Note: sectors scaled logarithmically due to large range of values)

To improve system-wide energy-efficiency, **focus on the dominant fraction**

**We make three arguments, in light of these observations**

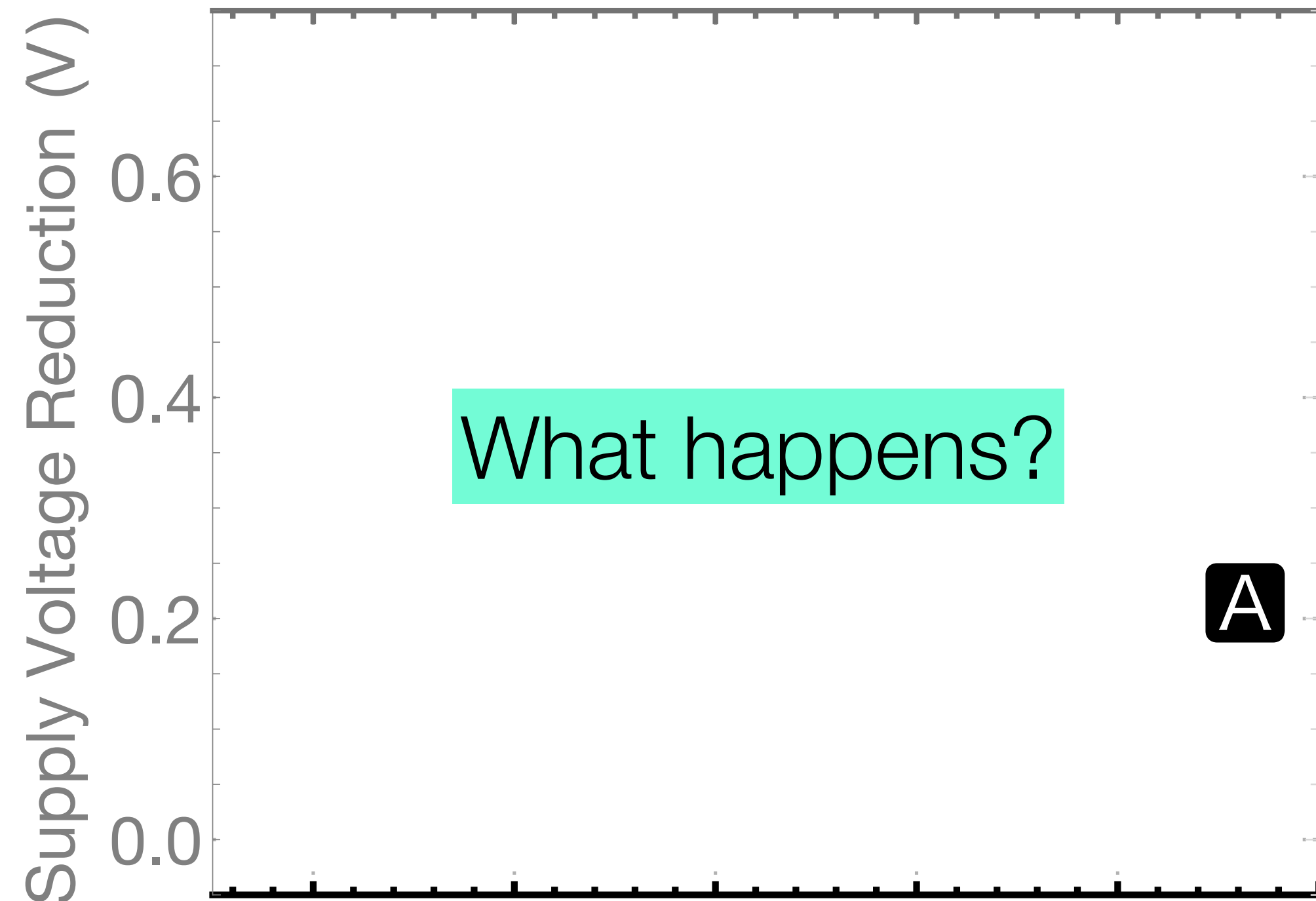
**For sensor-driven system such as wearables and “internet-of-(every)things”**

- ❶ Explore approximation in sensors / inputs
- ❷ Explore approximation in displays / outputs / communication

**Across all system types:**

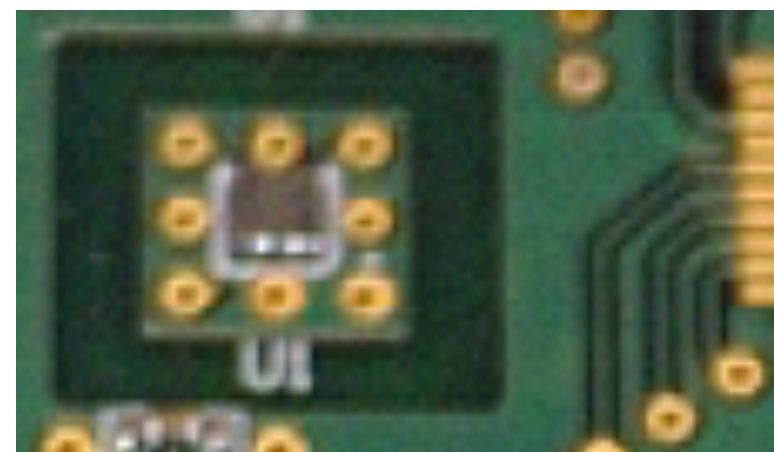
- ❸ What are bounds on benefit from reducing precision, accuracy, reliability/certainty?  
(If best-case achievable gains are small, why bother...)

# 1 Approximate Inputs

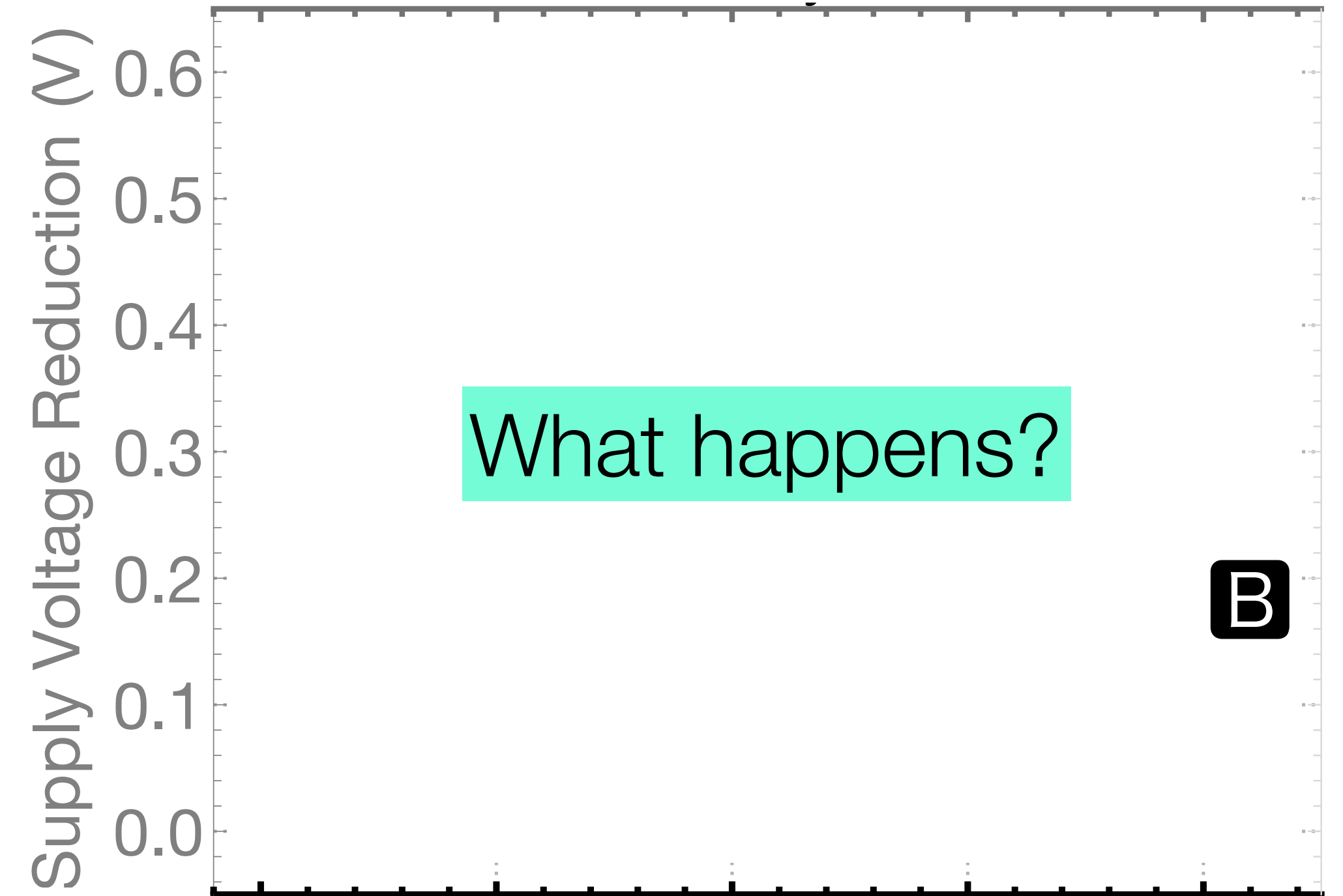


Errors (per  $10^3$  Readings)

A

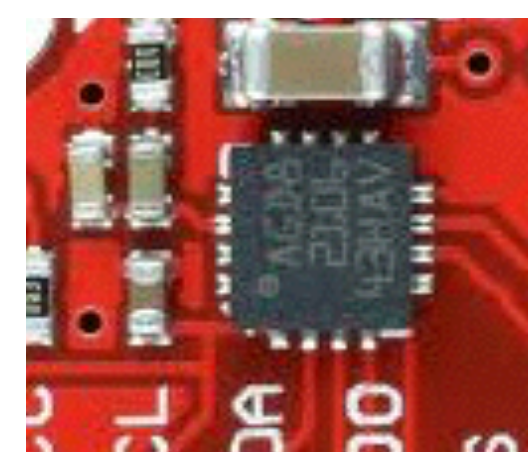


TI TMP006  
IR Temperature  
Sensor



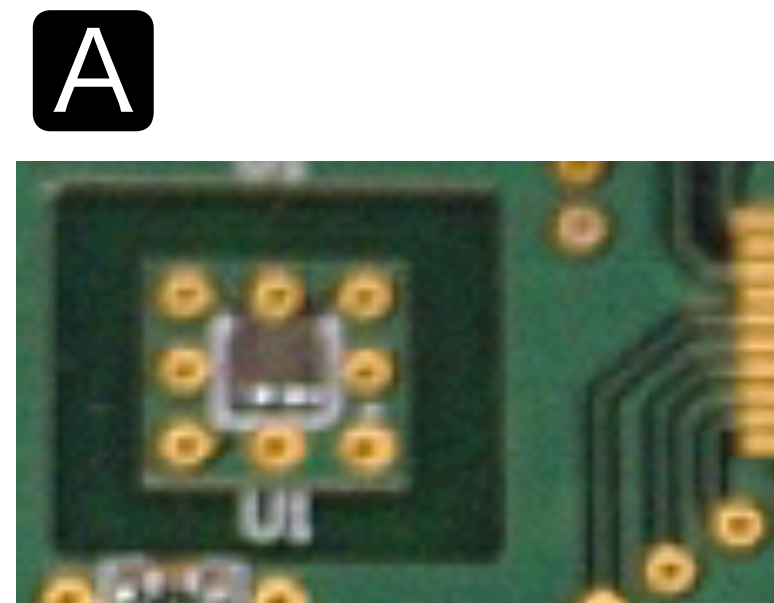
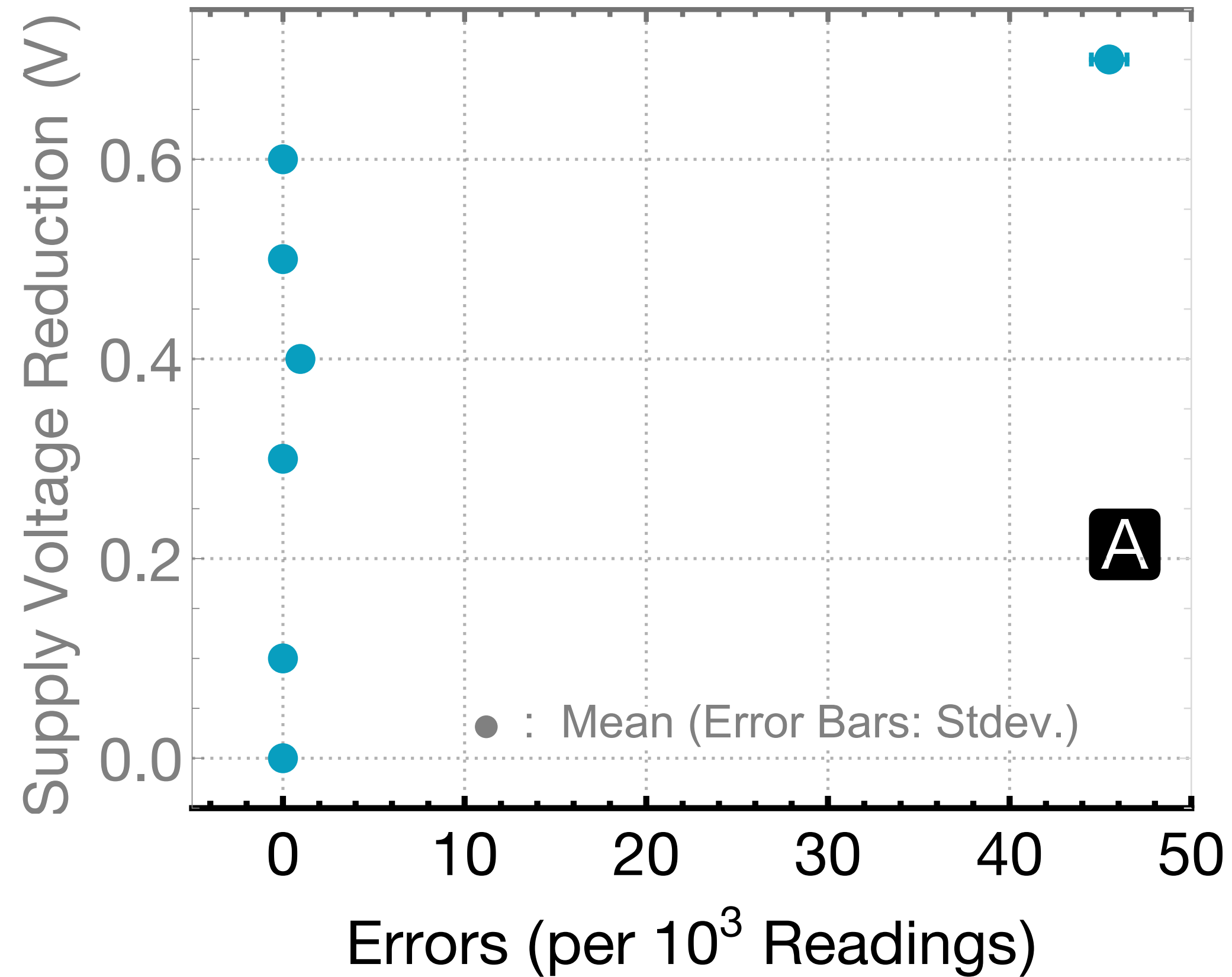
Errors (per  $10^3$  Readings)

B

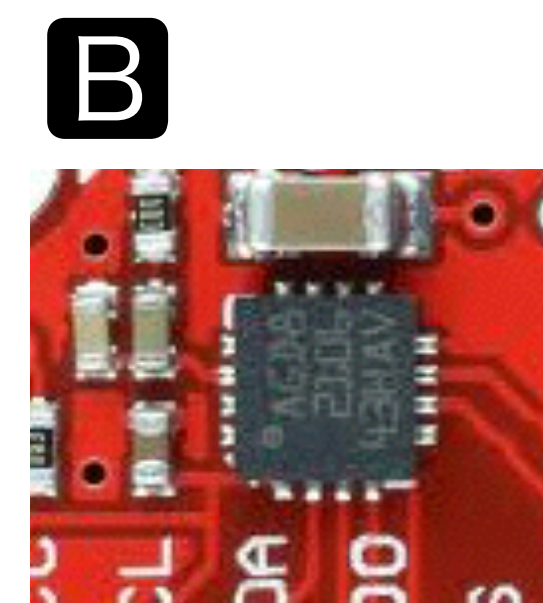
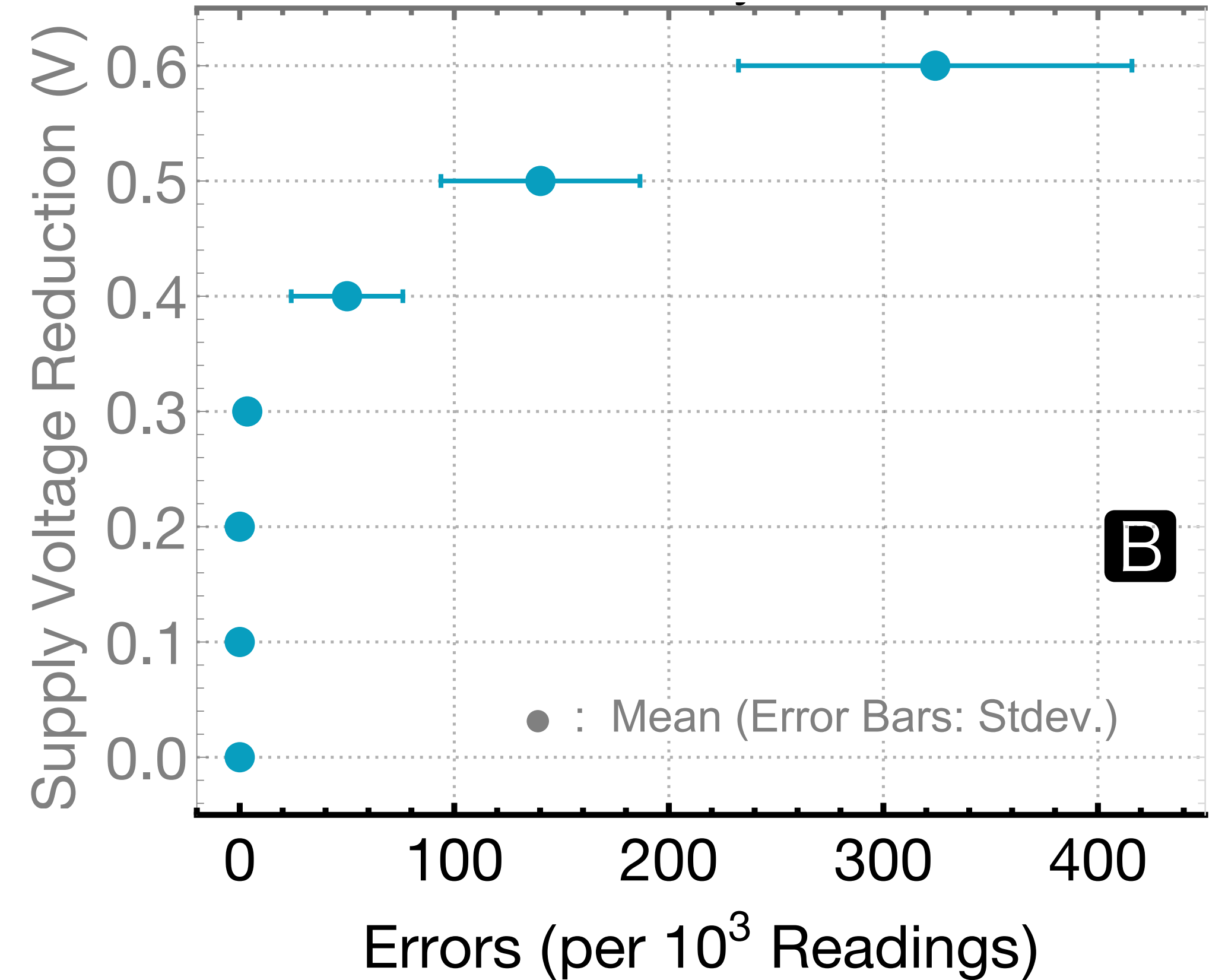


ST L3G4200D  
Gyroscope  
Sensor

# 1 Approximate Inputs



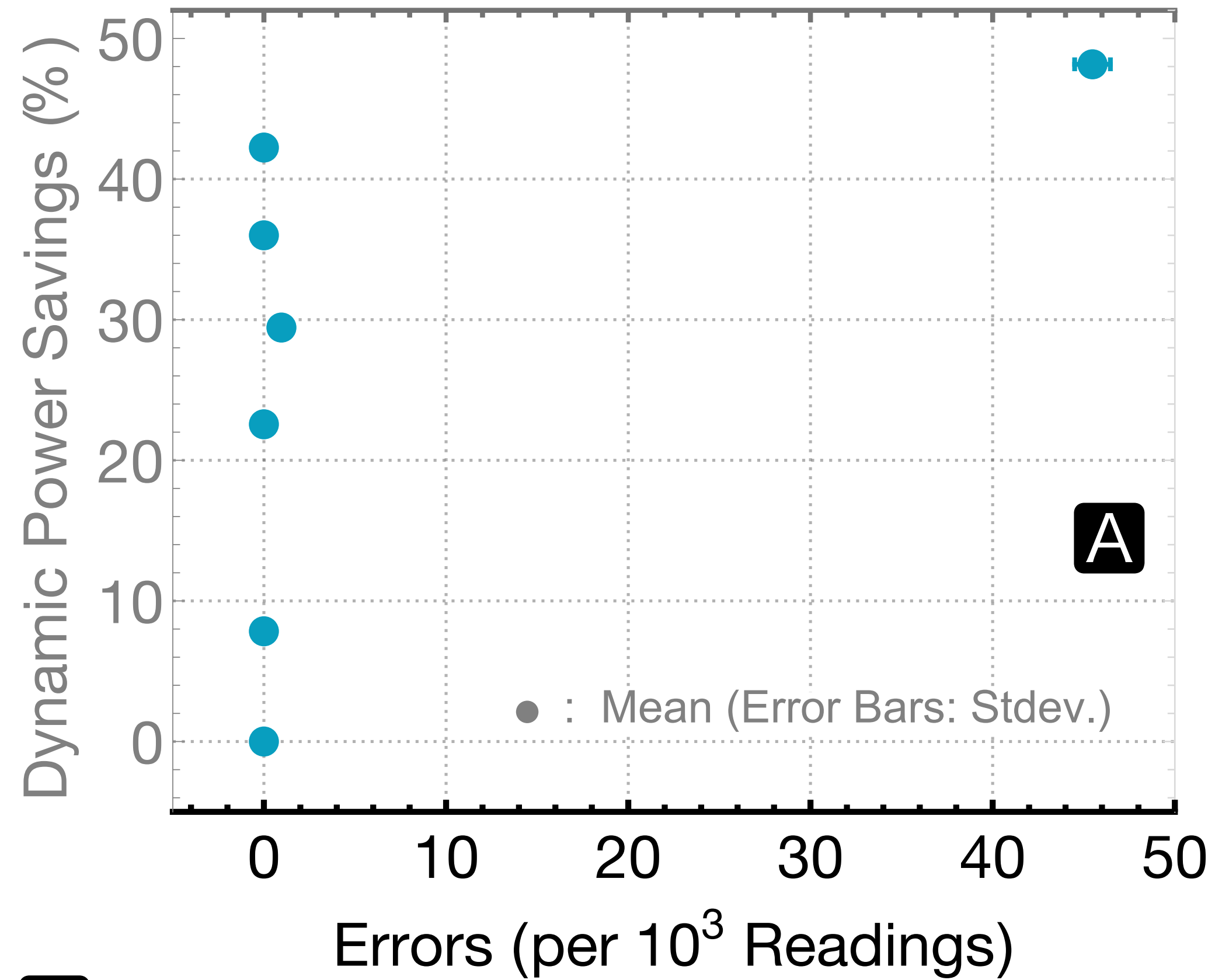
TI TMP006  
IR Temperature  
Sensor



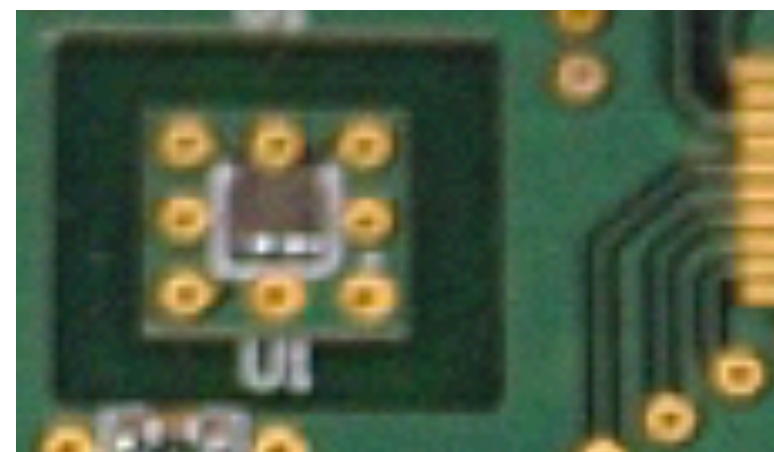
ST L3G4200D  
Gyroscope  
Sensor



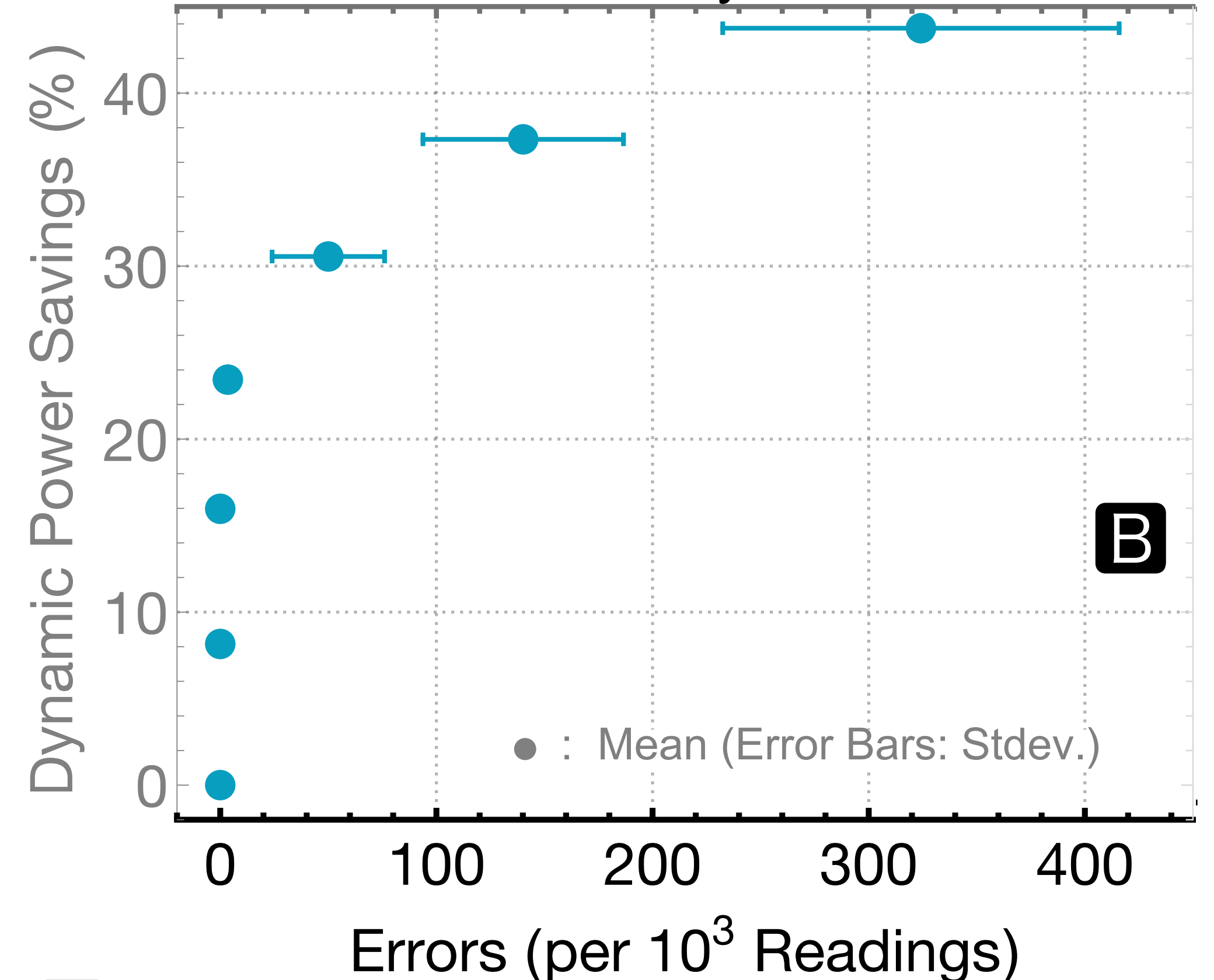
# 1 Approximate Inputs



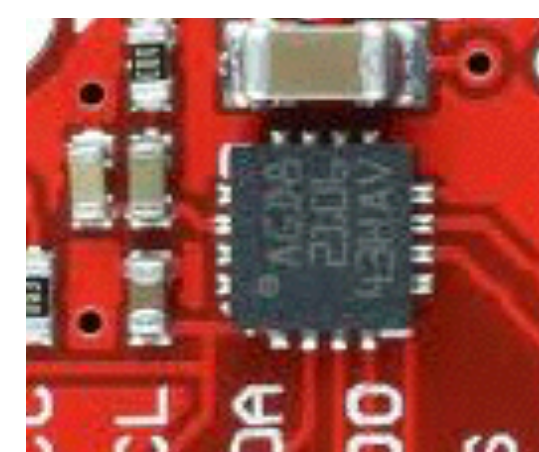
**A**



TI TMP006  
IR Temperature  
Sensor



**B**

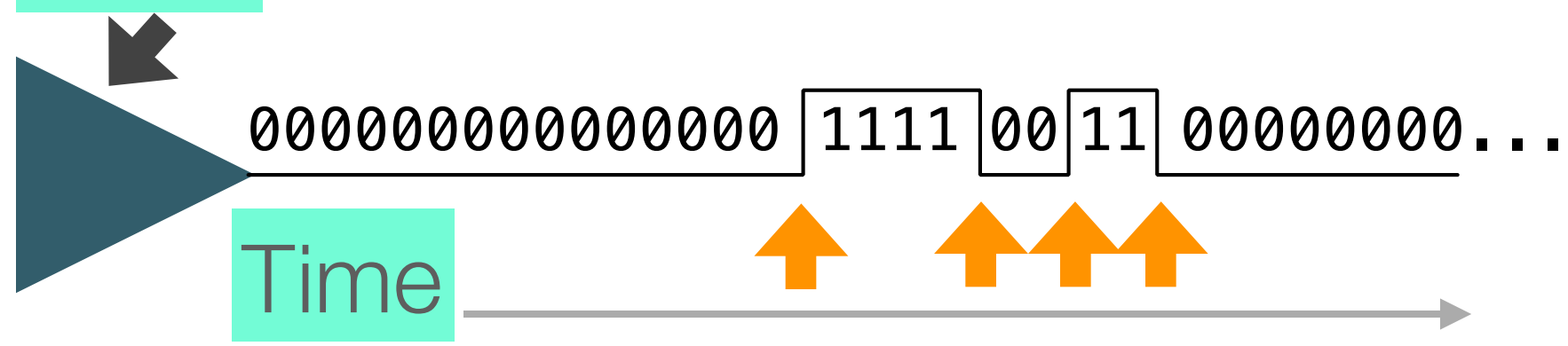


ST L3G4200D  
Gyroscope  
Sensor

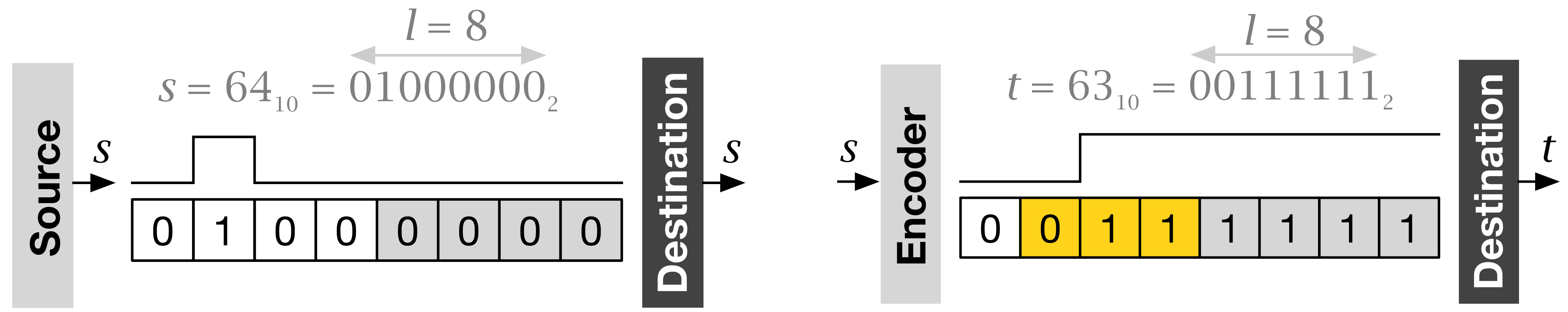


# ② Approximate Outputs / Communication

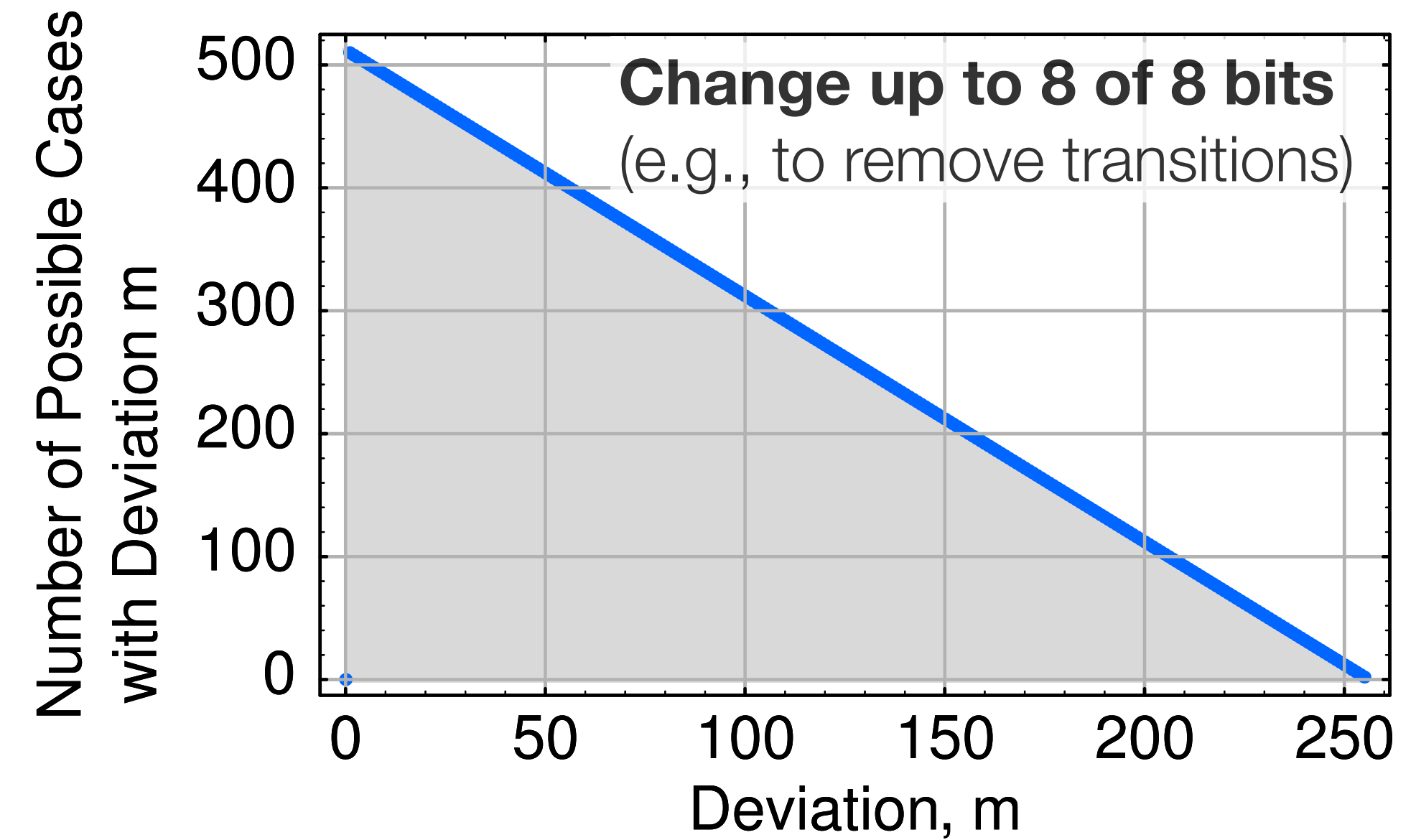
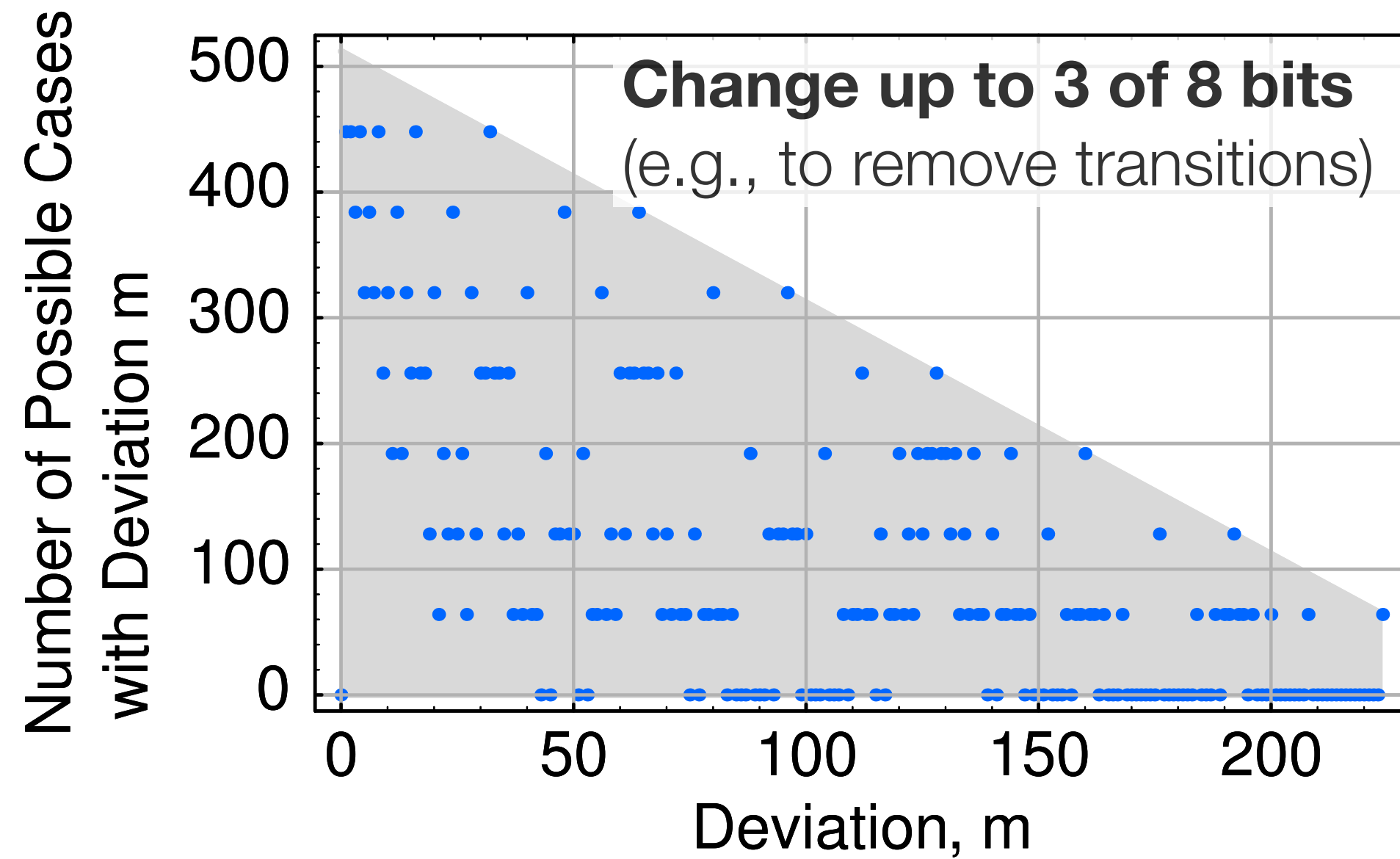
Serial communication interface/bus driver



**More transitions : more dynamic power dissipation**



# ③ Bounds: How Badly (and Often) Do Values Deviate?



**Count of cases** is given by number of solutions to Diophantine equation pair:

( $w$  and  $v$  are two  $L$ -bit words)

$$\left| \sum_{i=0}^{L-1} w_i 2^i - \sum_{i=0}^{L-1} v_i 2^i \right| = m$$

↑  
**Deviation from correctness**

$$\sum_{i=0}^{L-1} (w_i(1 - v_i) + v_i(1 - w_i)) = k$$

↑  
**Number of bits we perturb**

An **upper bound on number of cases** is  $2^{L+1} - 2m$  (shaded gray region in plots above)

## Approximating Outside the Processor

- ① We should **focus on the domains that will be important** in the future
- ② For the important domains, we should **focus on bottlenecks**
- ③ For the growing domain of IoT/wearables: **sensors, displays, communication**
- ④ **Understanding upper limits of benefits** informs choice of realistic techniques



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